

הפקולטה להנדסת חשמל אלקטרוניקה מחשבים תקשורת

Guest Lecture - הרצאת אורח

You are invited to attend a lecture by

הנכם מוזמנים להרצאה של

Zvi Or-Bach

President and CEO of MonolithIC 3D[™] Inc.

בנושא:

Monolithic 3D Integrated Circuits

In this talk, I will discuss the issue of monolithic 3D ICs, including challenges, approaches and the current state of affairs. The talk will be based on, and probably biased by, my own undertaking in this field and the work done at MonolithIC3D.

The outline of the talk is as follows:

A. Dimension scaling - the point of dimension return has arrived

B. Moore's Law is alive and well for the next two decades thanks for monolithic 3D

1) What is it?

- 2) Challenges and solutions
- 3) Monolithic 3D is far more than Moore
- 4) Monolithic 3D is already here IEDM 2012 the inflection point
- 5) Discussion Q&A

About the speaker

Zvi Or-Bach is the founder, president and CEO of MonolithIC 3D[™] Inc., declared Top Embedded Innovator-Silicon by Embedded Computing Design Magazine and Finalist of the "Best of Semicon West 2011" for its monolithic 3D-IC breakthrough. Or-Bach was also a finalist of the EE Times Innovator of the Year Award in 2011 and 2012 for his pioneering work on the monolithic 3D-IC.

Or-Bach has a history of innovative development in fast-turn ASICs for over 20 years. His vision led to the invention of the first Structured ASIC architecture, the first single via programmable array, and the first laser-based system for oneday Gate Array customization. In 2005, Or-Bach won the EETimes Innovator of the Year Award and was selected by EE Times to be part of the "Disruptors" -- "The People, Products and Technologies That Are Changing The Way We Live, Work and Play".

Prior to MonolithIC 3D, Or-Bach founded eASIC in 1999 and served as its CEO. Earlier, he founded Chip Express in 1989 and served as the company's President and CEO

Zvi Or-Bach received his B.Sc. degree (1975) cum laude in Electrical Engineering from the Technion - Israel Institute of Technology, and M.Sc. (1979) with distinction in Computer Science, from the Weizmann Institute, Israel. He holds over 150 issued or pending patents, primarily in the field of 3D integrated circuits and semi-custom chip architectures. He is the Chairman of the Board for Zeno Semiconductors, Bioaxial and VisuMenu.

The lecture will take place on Tuesday, 1/1/2013 at 10:30 in room 1007 Electrical Eng. Building Technion City ההרצאה תתקיים ביום שלישי, 1/1/2013 בשעה 10:30 בחדר 1007 בבניין הפקולטה להנדסת חשמל קריית הטכניון

כיבוד קל יוגש לפני ההרצאה